

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	
)	
Eiichi HORITA et al.)	Art Unit: Not Yet Assigned
)	
Serial No. Not Yet Assigned)	
(National Phase of PCT/JP2005/015085))	Examiner: Not Yet Assigned
)	
Filed: July 21, 2006)	
)	
For: Event-Ordering Certification Method)	Attorney Docket No.: 44471/332867

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Respectfully submitted,



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Sheet	1	of	2
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Application Number	PCT/JP2005/015085
Filing Date	July 20, 2006
First Named Inventor	Eiichi HORITA et al.
Art Unit	2431
Examiner Name	
Attorney Docket Number	44471/332867

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

/Shin Hon Chen/

Date Considered

04/29/2009

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Sheet 2 of 2

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Application Number PCT/JP2005/015085/32
Filing Date July 20, 2006
First Named Inventor Eiichi HORITA et al.
Art Unit 2431
Examiner Name
Attorney Docket Number 44471/332867

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	3	MASASHI UNE, KANTA MATSUURA, AKIRA TAKURA, "Digital Timestamp Gijyutsu no Genjo to Kadai", IMES Discussion Paper Series, Discussion Paper No. 99-J-36, Institute for Monetary and Economic Studies, Bank of Japan, 1999 October, pages 7 to 20, 26 to 28, 40 to 42, http://www.imes.boj.or.jp/jdps99/99-J-36.pdf	
	4	EIICHI HOTTA, SATOSHI ONO, and HIDETAKA ISHIMOTO, "Implementation Mechanics of Scalable Event-Ordering System without Single Point of Attack" ("Scalable de Tan'itsu Kogekiten no Nai Ivent Junjo Shomei System Jitsugen Kiko"), The Institute of Electronics, Information and Communication Engineers Gijyutsu Kenkyu Hokoku ISEC 2004-85-97, The Institute of Electronics, Information and Communication Engineers, 02 November, 2004, Vol. 104, No. 422, pages 1 to 8	X
	5	SHINGO ORIHARA, SATOSHI ONO, EIICHI HOTTA, and HIDETAKA ISHIMOTO, "Threat analysis of Event-Ordering Systems" ("Ivent Junjo Shomei System no Kyoji Bunseki"), The Institute of Electronics, Information and Communication Engineers Gijyutsu Kenkyu Hokoku ISEC2004-98-105, The Institute of Electronics, Information and Communication Engineers, 10 December, 2004, Vol. 104, No. 527, pages 39 to 45	X
	6	STUART HABER and W. SCOTT STORNETTA, "How to Time-Stamp a Digital Document", Journal of Cryptology, Vol. 3, No. 2, pp. 99-111, 1991	
	7	J.-J. QUISQUATER, H. MASSIAS, J. SERRET AVILA, B. PRENEEL, and B. VAN ROMPAY, "Specification and implementation of a timestamping system", Technical Report of Universite Catholique de Louvain, 1999, URL: http://www.dice.ucl.ac.be/crypto/TIMESEC/TR4.tgz	
	8	AHTO BULDAS, HELGER LIPMAA, and BERRY SCHOENMAKERS, "Optimally Efficient Accountable Time-Stamping", in Proceedings of Public Key Cryptography 2000 (PKC2000), eds. Y. Zheng and H. Imai, pp. 293-305, Springer-Verlag, January 2000	
	9	AHTO BULDAS, PEETER LAUD, HELGER LIPMAA, and JAN VILLEMSON, "Time-Stamping with Binary Linking Schemes", in Proceedings of Advances on Cryptology (CRYPTO'98), ed. H. Krawczyk, pp. 486-501, Springer-Verlag, 1998	

Examiner Signature	/Shin Hon Chen/	Date Considered	04/29/2009
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